

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6008794 A	19991228	19	Flat-panel display controller with improved dithering and frame rate control	345/598	345/572; 345/694	
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6008794 A	19991228	19	Graphics controller for generating flat-panel display signals in response to pixel data to cause display of images on flat-panel type display			

22/4/7, 6/6

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Ishii, Takatoshi	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6008794	<input type="checkbox"/>
2	ISHII, T	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6008794	<input type="checkbox"/>

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1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020041243 A1	20020411	11	Noise shaper	341/131		
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020005831 A1	20020117	20	FLAT-PANEL DISPLAY CONTROLLER WITH IMPROVED DITHERING AND FRAME RATE CONTROL	345/89		
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6362834 B1	20020326	20	Flat-panel display controller with improved dithering and frame rate control	345/690	345/596; 345/691	
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6288698 B1	20010911	13	Apparatus and method for gray-scale and brightness display control	345/87	345/204; 345/589; 348/714; 348/715; 348/716	
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6147633 A	20001114	16	Analog-to-digital converter having offset removing function	341/143	341/131	
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6008794 A	19991228	19	Flat-panel display controller with improved dithering and frame rate control	345/598	345/572; 345/694	
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5903482 A	19990511	42	Sampling frequency converting system and a method thereof	708/313	341/61	
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5530443 A	19960625	30	Digital circuit for the introduction of dither into an analog signal	341/131	341/139	
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5510790 A	19960423	25	Digital circuit for the introduction of dither into an analog signal	341/131	341/139; 341/155	
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5404427 A	19950404	15	Video signal processing with added probabilistic dither	345/597	345/428; 345/593; 345/605	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Fukuhara, Yukio et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20020041243	<input type="checkbox"/>
2	ISHII, TAKATOSHI	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20020005831	<input type="checkbox"/>
3	Ishii, Takatoshi	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6362834	<input type="checkbox"/>
4	Ishii, Takatoshi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6288698	<input type="checkbox"/>
5	Ukawa, Masayuki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6147633	<input type="checkbox"/>
6	Ishii, Takatoshi	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6008794	<input type="checkbox"/>
7	Iwamura, Hiroshi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5903482	<input type="checkbox"/>
8	Borgen, Gary S. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5530443	<input type="checkbox"/>
9	Borgen, Gary S. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5510790	<input type="checkbox"/>
10	Cawley, Robin A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5404427	<input type="checkbox"/>

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11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5175807 A	19921229	15	Video signal processing with added probabilistic dither	345/428	345/600; 348/655	
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4937576 A	19900626	11	Dither circuit responsive to zero input signal level	341/131	341/126	
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4916449 A	19900410	17	Wide dynamic range digital to analog conversion method and system	341/131	341/141; 341/144	
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4914439 A	19900403	8	Analog to digital conversion system utilizing dither	341/131	341/161	
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4857927 A	19890815	6	Dither circuit having dither level changing function	341/131	341/144; 341/155; 341/200	
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4845498 A	19890704	16	Wide dynamic range digital to analog conversion method and systems	341/131	341/122; 341/141; 341/144	
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4812846 A	19890314	9	Dither circuit using dither including signal component having frequency half of sampling frequency	341/131		
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4374438 A	19830215	12	Digital frequency and phase lock loop	455/265	331/1A; 331/17; 342/187; 375/376; 377/45	
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4306284 A	19811215	30	Optimizer industrial test unit	700/38	123/487; 701/99	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
11	Cawley, Robin A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5175807	<input type="checkbox"/>
12	Yoshio, Junichi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4937576	<input type="checkbox"/>
13	Kubo, Mitsumasa et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4916449	<input type="checkbox"/>
14	Nakahashi, Teruyoshi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4914439	<input type="checkbox"/>
15	Takabayashi, Osamu	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4857927	<input type="checkbox"/>
16	Kubo, Mitsumasa et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4845498	<input type="checkbox"/>
17	Noro, Masao	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4812846	<input type="checkbox"/>
18	Crowley, Albert T.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4374438	<input type="checkbox"/>
19	Malcolm, Donald H.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4306284	<input type="checkbox"/>

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20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4220923 A	19800902	14	Adaptive interference reduction system for crosstalk cancellation in a dual polarization system	455/295	327/553; 333/166; 333/18; 375/348; 455/60; 455/63	
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 3562420 A	19710209	8	PSUEDORANDOM QUANTIZING SYSTEMS FOR TRANSMITTING TELEVISION SIGNALS	348/607	375/130; 375/240.03; 375/245; 375/362	
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 07177101 A	19950714	7	QUANTIZER			
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 05145376 A	19930611	8	DIGITAL FILTER		708/270	
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 01288016 A	19891120	5	ANALOG/DIGITAL CONVERSION CIRCUIT		341/143	
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 62183627 A	19870812	3	PROCESSING SYSTEM FOR DIGITAL DATA			
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4220923 A	19800902	14	Adaptive interference reduction system for crosstalk cancellation in a dual polarization system			
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	WO 200063786 A	20001026	1	Embedded signaling apparatus for unauthorized copying prevention of compact disks, produces output signal consisting of audio data and embedded data on receiving composite audio dither signal from dither adder			

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
20	Pelchat, Guy M. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4220923	<input type="checkbox"/>
21	Thompson, John E	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 3562420	<input type="checkbox"/>
22	ICHIMURA, Hajime et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 07177101 A	<input type="checkbox"/>
23	NISHIO, FUMITAKA	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 05145376 A	<input type="checkbox"/>
24	NAKA, HIDEYUKI et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 01288016 A	<input type="checkbox"/>
25	NINOMIYA, TAKESHI et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 62183627 A	<input type="checkbox"/>
26	BAIRD, CHARLES A et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4220923	<input type="checkbox"/>
27	STEBBINGS, D	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	2001-265525	<input type="checkbox"/>

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28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 07129763 A	20000615	10	Dither matrix element generator for image processing - uses logical element to perform logical operation of specific bit in line address and corresponding bit in sequence address for each specific bit			
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 05335964 A	20000221	8	Delta sigma demodulator using rectangular wave as dither - has adder connected to delay circuit through integral circuit and quantiser; adder receiving second input from D=A converter as feedback signal NoAbstract			
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	JP 03020807 A	19910129	6	Low cost linear solenoid controller - has current value memory, corrector, dither adder, average value calculator and processor NoAbstract Dwg 2/9			
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4857927 A	19890815	6	Dither circuit having dither level changing function - controls level of dither in accordance with level of input signal to A=D converter so level of other increases as input signal decreases			

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
28	CHOI, B et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5495346	<input type="checkbox"/>
29		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 05335964 A	<input type="checkbox"/>
30		<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	JP 03020807 A	<input type="checkbox"/>
31	TAKABAYASH, O	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4857927	<input type="checkbox"/>

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1	20030703	6	System and method for dithering with reduced memory	US 20030122847 A1	345/616
2	20030605	14	Method for processing video data for a display device	US 20030103059 A1	345/596
3	20030403	13	Apparatus and method for dithering in image processing and computer graphics systems	US 20030063100 A1	345/596
4	20030109	31	Display device	US 20030006994 A1	345/596
5	20021205	21	Display device and display panel driving method	US 20020180754 A1	345/598
6	20021107	27	Method for halftoning using a difference weighting function	US 20020163528 A1	345/596
7	20021024	23	Transmission of digital data from a screen	US 20020154137 A1	345/596
8	20020117	48	Recovering added precision from L-bit samples by dithering the samples prior to an averaging computation	US 20020005854 A1	345/596
9	20011129	14	Tone modifying dithering system	US 20010045957 A1	345/596
10	20031216	304	Dot adjacency compensation in optical storage systems using ink dots	US 6665454 B1	382/299

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	345/596; 382/252; 382/270; 382/275
1	
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10	345/596; 345/698; 358/1.7; 358/1.8; 358/448; 358/453; 382/267; 382/274; 382/282

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11	20030812	12	Pattern dithering	US 6606166 B1	358/1.9
12	20030729	21	Image format conversion such as photometric, rotation, cropping, padding, scaling, dithering, bit padding, grayscale and color transformation, encoding and decoding using a plurality of filters	US 6600840 B1	382/302
13	20030701	18	Method of an apparatus for distinguishing type of pixel	US 6587115 B2	345/596
14	20030506	17	Method for transferring image information, method for updating image information, transferring apparatus and updating apparatus	US 6559855 B1	345/596
15	20021029	16	Method for detecting an image edge within a dithered image	US 6473525 B1	382/199

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11	345/597; 358/3.13; 358/518; 382/167
12	345/596; 345/649; 345/660; 358/3.13; 382/162; 382/232; 382/261; 382/282; 382/296; 382/298
13	382/173; 382/174
14	345/619; 345/694
15	345/596; 345/613; 348/574; 358/3.13; 358/426.01; 382/218; 382/260; 382/263; 382/264; 382/266; 382/269; 382/282; 382/298; 382/299

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16	20020709	28	Light modulating devices	US 6417864 B1	345/596
17	20020326	20	Flat-panel display controller with improved dithering and frame rate control	US 6362834 B1	345/690
18	20020319	10	Multisample dither method with exact reconstruction	US 6359626 B1	345/596
19	20020129	29	Method and apparatus for modeling and reconstruction of halftoned images	US 6343159 B1	382/284
20	20011211	23	Method for generating dither values for one-dimensional dither arrays	US 6330368 B1	382/251
21	20010220	15	Method and apparatus for texture level of detail dithering	US 6191793 B1	345/582
22	20010206	19	Method for reconstructing a dithered image	US 6185334 B1	382/199
23	20001128	17	System and method for performing dithering with a graphics unit having an oversampling buffer	US 6154195 A	345/596
24	20000926	32	Force field halftoning	US 6124844 A	345/596
25	20000912	14	Dual displays having independent resolutions and refresh rates	US 6118413 A	345/596

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16	345/598; 345/599
17	345/596; 345/691
18	345/549; 345/600; 345/605
19	345/596; 348/584; 382/294
20	345/596; 358/1.9; 358/3.13; 382/252
21	345/596
22	345/596; 382/260; 382/264; 382/266
23	
24	358/3.14; 382/252
25	345/3.1

	Issue Date	Pages	Title	Document ID	Current OR
26	20000815	22	Method and system for displaying an image at a desired level of opacity	US 6104377 A	345/596
27	20000718	23	Drive apparatus for self light-emitting display	US 6091398 A	345/204
28	20000704	19	Image display for dither halftoning	US 6084560 A	345/89
29	20000530	54	Image processor using both dither and error diffusion to produce halftone images with less flicker and patterns	US 6069609 A	345/596
30	20000418	10	Methods and apparatus for processing data values representative of an image with efficient dither matrices	US 6052113 A	345/596
31	20000418	21	Gradation display system	US 6052112 A	345/596
32	20000222	14	Multicolor display control method for liquid crystal display	US 6028588 A	345/589
33	19991228	19	Flat-panel display controller with improved dithering and frame rate control	US 6008794 A	345/598
34	19990518	19	System and method for dithering and quantizing image data to optimize visual quality of a color recovered image	US 5905504 A	345/597

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26	345/643; 345/690
27	345/598; 345/690; 358/1.9
28	345/596; 345/599
29	345/616; 345/690; 358/3.03; 382/252
30	345/598
31	345/89
32	345/593; 345/596; 345/600; 345/605; 345/89
33	345/572; 345/694
34	

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35	19990518	9	Generating color-correction look-up-table addresses by multi-level half-toning	US 5905490 A	345/597
36	19981215	21	Concurrent dithering and scale correction of pixel color values	US 5850208 A	345/600
37	19980915	18	Image display using irregularly placed curving structures	US 5808622 A	345/596
38	19980721	22	Image display using evenly distributed intensity clusters	US 5784049 A	345/599
39	19980707	11	Image generation device and method using dithering	US 5777599 A	345/596
40	19980331	14	Method and apparatus for dithering images in a digital display system	US 5734369 A	345/605
41	19980203	33	Apparatus and method for generating halftoning or dither values	US 5714975 A	345/596
42	19970902	12	System and method for generating a universal palette and mapping an original color space to the universal palette	US 5664080 A	345/593
43	19970715	18	System and method for dithering and quantizing image data to optimize visual quality of a color recovered image	US 5649083 A	345/597
44	19970408	19	System and method for real-time image display palette mapping	US 5619230 A	345/597

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35	345/89
36	345/597; 345/694
37	
38	358/3.17
39	
40	345/597
41	358/3.13
42	345/597; 345/598; 345/605
43	345/605
44	

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45	19970128	42	Method and apparatus for improved color recovery in a computer graphics system	US 5598184 A	345/597
46	19961126	14	Image display apparatus and method using irregularly placed curving structures	US 5579457 A	345/596
47	19961105	10	Image formation system invalidating outline correction within gradation image region	US 5572638 A	345/613
48	19960402	9	Method and apparatus for improved area demarcation in bit mapped image derived from multi-color bit mapped image	US 5504846 A	345/597
49	19960227	10	Element generator for dither matrix and a dithering apparatus using the same	US 5495346 A	358/3.13
50	19960116	22	Method and system for displaying color on a computer output device using dithering techniques	US 5485558 A	345/597
51	19951003	9	Method and apparatus for mapping colors in an image through dithering and diffusion	US 5455600 A	345/597

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45	
46	
47	345/599
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49	326/104; 326/52; 327/76; 340/146.2; 345/55; 345/596; 348/574; 358/3.23; 382/270; 708/625
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52	19950404	15	Video signal processing with added probabilistic dither	US 5404427 A	345/597
53	19941206	24	Method and apparatus for non-linear dithering of digital images	US 5371515 A	345/596
54	19940607	19	Image enhancement with mask having fuzzy edges	US 5319742 A	345/601
55	19940405	19	Window-relative dither circuit	US 5301269 A	345/599
56	19931214	39	Image processing apparatus for estimating halftone images from bilevel and pseudo halftone images	US 5271095 A	345/428
57	19931123	26	Method and apparatus for vector aligned dithering	US 5264840 A	345/599
58	19930406	21	Method and apparatus for dithering graphic images	US 5201030 A	345/596
59	19930112	9	Rendering shaded areas with boundary-localized pseudo-random noise	US 5179641 A	345/596
60	19921117	35	Method and apparatus for the dithering of antialiased vectors	US 5164717 A	345/596
61	19920505	20	Artificial halftone processing apparatus	US 5111194 A	345/690
62	19900918	19	Apparatus and system for generating smooth shaded continuous tone images	US 4958272 A	345/597

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52	345/428; 345/593; 345/605
53	358/3.24
54	345/427; 345/597; 345/602
55	
56	345/589; 345/596; 345/643
57	358/3.14
58	345/690; 358/3.14
59	345/443
60	345/611; 358/3.07; 358/3.08
61	345/566; 345/572; 345/596; 358/3.22; 358/3.23
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63	19900911	13	Display using ordered dither	US 4956638 A	345/597
64	19900417	56	Electronic graphic arts screener	US 4918622 A	345/597
65	19871110	13	Half-toning implementation for interactive image editing	US 4706077 A	345/599
66	19871006	35	Method of processing gradation information with variable magnification	US 4698778 A	345/667
67	19870623	28	Method of processing gradation information	US 4675831 A	345/597
68	19760601	16	Bi-level display system	US 3961134 A	348/797
69	19760210	13	Animated dithered display systems	US 3937878 A	348/798

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63	358/3.13
64	
65	358/3.23
66	345/599; 358/3.07; 358/451
67	348/825
68	315/169.4; 345/596
69	315/169.4; 345/473; 345/596; 348/22

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1	20030605	14	Method for processing video data for a display device	US 20030103059 A1	345/596	
2	20030227	17	Display apparatus	US 20030038758 A1	345/63	
3	20030109	34	Method of driving plasma display panel	US 20030006715 A1	315/169.3	
4	20021219	17	Method and apparatus for processing video picture data for display on a display device	US 20020190931 A1	345/63	
5	20020926	60	Display device, and display method	US 20020135595 A1	345/589	
6	20020509	60	METHOD OF DRIVING PLASMA DISPLAY PANEL	US 20020054000 A1	345/60	
7	20020214	22	Plasma display panel driving method	US 20020018030 A1	345/60	
8	20020207	23	Plasma display panel driving method	US 20020014847 A1	315/169.1	315/169.3
9	20020131	23	Plasma display panel driving method	US 20020012075 A1	348/797	
10	20020117	127	Floor controller for real-time control of music signal processing, mixing, video and lighting	US 20020005111 A1	84/645	84/477R
11	20031111	76	Method for driving a plasma display panel	US 6646625 B1	345/63	315/169.4; 345/60; 345/67
12	20031104	22	Plasma display panel driving method	US 6642911 B2	345/60	345/41; 345/690
13	20030923	33	Method of driving plasma display panel	US 6624588 B2	315/169.1	315/169.4; 345/63; 345/67
14	20030902	57	Method of driving plasma display panel	US 6614413 B2	345/63	345/68; 345/690

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15	20021203	16	Versatile video transformation device	US 6489997 B1	348/441	348/445; 348/446; 348/458; 348/556
16	20021105	39	Luminance resolution enhancement circuit and display apparatus using same	US 6476824 B1	345/690	345/694
17	20021015	22	Plasma display panel driving method	US 6465970 B2	315/169.4	345/76
18	20020709	40	Method of driving plasma display panel	US 6417824 B1	345/60	345/93
19	20020521	32	Method for driving a plasma display panel	US 6392616 B1	345/60	345/208; 345/58; 348/537; 348/625
20	20010116	32	Method for driving a plasma display panel	US 6175194 B1	315/169.4	315/169.1; 345/67; 345/68
21	20000718	23	Drive apparatus for self light-emitting display	US 6091398 A	345/204	345/598; 345/690; 358/1.9
22	20000418	21	Gradation display system	US 6052112 A	345/596	345/89
23	19991228	12	Drive apparatus for self light emitting display unit	US 6008793 A	345/204	345/600; 348/793
24	19970729	14	Systems for dither-quantizing and reconstruction of digital television signals	US 5652624 A	375/240.01	348/574; 386/1; 386/46
25	19960806	22	Video telephone systems	US 5543939 A	375/240.02	348/14.01; 358/466; 379/88.13
26	19950404	15	Video signal processing with added probabilistic dither	US 5404427 A	345/597	345/428; 345/593; 345/605
27	19921229	15	Video signal processing with added probabilistic dither	US 5175807 A	345/428	345/600; 348/655

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28	19920915	14	Television systems transmitting dither-quantized signals	US 5148273 A	348/390.1	
29	19880719	17	Cinematic dithering for television systems	US 4758893 A	348/472	348/574
30	19870331	27	Analog television with regenerable and encryptable signals	US 4654704 A	380/216	380/200; 380/239; 380/44
31	19861014	27	Compatible color television with regenerable signals	US 4617597 A	386/45	380/210; 380/216; 380/240
32	19860204	28	Compatible color television with regenerable signals	US 4568966 A	386/21	380/210; 380/216; 380/240; 386/45
33	19810519	11	Image coding	US 4268861 A	375/240.1	375/144; 375/245
34	19730612	11	ORDERED DITHER SYSTEM	US 3739082 A	348/574	375/240.21

	Type	L #	Hits	Search Text	DBs
1	BRS	L1	160286	video adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	64954	grayscale or greyscale or half-tone or gradation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	102	reverse adj gamma adj correction	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	68	dither near coefficient\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	2211	dither near matrix\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	135419	adder	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	21	(sum or total) and 4 and zero	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	10	dither adj coefficient adj generator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L9	3	dither adj coefficient adj pattern	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L10	181	dither adj signal adj generator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	0	2 and 3 and 10	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	526	345/690.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
13	BRS	L13	201	(345/596-599).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
14	BRS	L14	1420	(345/204).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
15	BRS	L15	857	(345/89).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
16	BRS	L16	422	(345/63).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
17	BRS	L17	112	(358/3.01).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
18	BRS	L18	179	(358/3.03).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
19	BRS	L19	86	(358/3.13).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
20	BRS	L20	75	(358/3.19).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
21	BRS	L21	3597	12 or 13 or 14 or 15 or 16 or 17 or 18 or 19 or 20	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
22	BRS	L22	3	2 and 10 and 21	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
23	BRS	L23	7	1 and 2 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
24	BRS	L24	6333	low adj (luminance or brightness)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

Type	L #	Hits	Search Text	DBs
25 BRS	L25	25355	high adj (luminance or brightness)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
26 BRS	L26	1294	dither near pattern\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
27 BRS	L27	30	(24 or 25) and 26	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
28 BRS	L28	44	2 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
29 BRS	L29	107	1 and 2 and 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
30 BRS	L30	39	26 and 29	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
31 BRS	L31	2577	(358/1.9).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
32 BRS	L32	693	(345/690-693).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
33 BRS	L33	700	(382/167).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
34 BRS	L35	111186	image adj processing	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
35 BRS	L36	1940619	weight\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
36 BRS	L37	1	dither adj coefficient adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
37	BRS	L38	1	2 and 3 and 5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
38	BRS	L39	14671	processing near 1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
39	BRS	L40	6780	31 or 32 or 33 or 21	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
40	BRS	L41	11	2 and 5 and (24 and 25)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
41	BRS	L42	1202	dither\$4 and luminance	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
42	BRS	L43	110	1 and 35 and 42	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
43	BRS	L44	7	40 and 43	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
44	BRS	L45	1401	dither\$4 and brightness	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
45	BRS	L46	29	1 and 5 and 45	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
46	BRS	L47	524	36 and 42	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
47	BRS	L48	196	1 and 47	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
48	BRS	L49	34	26 and 48	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

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1	20030902	57	Method of driving plasma display panel	US 6614413 B2	345/63	345/68; 345/690
2	20030715	44	Method for driving a plasma display panel	US 6593903 B2	345/60	345/61
3	20021217	27	Method for driving plasma display panel	US 6495968 B2	315/169.4	345/60; 345/63
4	20021119	22	Display device	US 6483248 B2	315/169.3	345/60
5	20020709	40	Method of driving plasma display panel	US 6417824 B1	345/60	345/93
6	20020702	41	Method for driving a plasma display panel	US 6414658 B1	345/63	345/208
7	20020521	32	Method for driving a plasma display panel	US 6392616 B1	345/60	345/208; 345/58; 348/537; 348/625
8	20011002	70	Half tone display method of display panel	US 6297788 B1	345/63	345/589; 345/67
9	20010116	32	Method for driving a plasma display panel	US 6175194 B1	315/169.4	315/169.1; 345/67; 345/68
10	20000718	23	Drive apparatus for self light-emitting display	US 6091398 A	345/204	345/598; 345/690; 358/1.9
11	19991228	12	Drive apparatus for self light emitting display unit	US 6008793 A	345/204	345/600; 348/793

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1	BRS	L1	2	5751920.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	2446	(358/1.9).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	1852	(358/1.9).ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	69	(358/3.13).ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	1404	dither and adder	USPAT; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L6	170	(345/596-599).ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L7	347	(345/63).ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L8	91	(358/3.01).ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L9	138	(358/3.03).ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L10	2602	2 or 4 or 6 or 7 or 8 or 9	USPAT; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L11	30	dither adj coefficient	USPAT; EPO; JPO; DERWENT; IBM_TDB
12	BRS	L12	11	weight\$4 and 11	USPAT; EPO; JPO; DERWENT; IBM_TDB

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1	20030227	17	Display apparatus	US 20030038758 A1	345/63	
2	20030123	123	Display driving method and display apparatus utilizing the same	US 20030016189 A1	345/55	
3	20030116	34	Image display apparatus	US 20030011545 A1	345/76	345/214; 345/690; 345/694; 345/77; 345/78
4	20030109	31	Display device	US 20030006994 A1	345/596	
5	20021219	33	Display apparatus	US 20020190940 A1	345/87	
6	20021219	17	Method and apparatus for processing video picture data for display on a display device	US 20020190931 A1	345/63	
7	20020117	20	FLAT-PANEL DISPLAY CONTROLLER WITH IMPROVED DITHERING AND FRAME RATE CONTROL	US 20020005831 A1	345/89	
8	20021217	27	Method for driving plasma display panel	US 6495968 B2	315/169.4	345/60; 345/63
9	20021105	39	Luminance resolution enhancement circuit and display apparatus using same	US 6476824 B1	345/690	345/694

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10	20021029	16	Method for detecting an image edge within a dithered image	US 6473525 B1	382/199	345/596; 345/613; 348/574; 358/3.13; 358/426.01; 382/218; 382/260; 382/263; 382/264; 382/266; 382/269; 382/282; 382/298; 382/299
11	20021008	10	Apparatus having a DAC-controlled ramp generator for applying voltages to individual pixels in a color electro-optic display device	US 6462728 B1	345/100	345/89
12	20020702	41	Method for driving a plasma display panel	US 6414658 B1	345/63	345/208
13	20020514	41	Plasma display panel drive pulse controller	US 6388678 B1	345/690	345/39; 345/41; 345/60; 345/691; 345/692; 345/693
14	20020326	20	Flat-panel display controller with improved dithering and frame rate control	US 6362834 B1	345/690	345/596; 345/691
15	20011211	23	Method for generating dither values for one-dimensional dither arrays	US 6330368 B1	382/251	345/596; 358/1.9; 358/3.13; 382/252
16	20011002	70	Half tone display method of display panel	US 6297788 B1	345/63	345/589; 345/67

	Issue Date	Page s	Title	Document ID	Current OR	Current XRef
17	20010911	13	Apparatus and method for gray-scale and brightness display control	US 6288698 B1	345/87	345/204; 345/589; 348/714; 348/715; 348/716
18	20010403	22	Method for reducing pulsing on liquid crystal displays	US 6211859 B1	345/596	
19	20010306	22	"Frame-rate modulation method and apparatus to generate flexible grayscale shading for super twisted nematic displays using stored brightness-level waveforms"	US 6198469 B1	345/690	345/89
20	20010206	19	Method for reconstructing a dithered image	US 6185334 B1	382/199	345/596; 382/260; 382/264; 382/266
21	20000718	23	Drive apparatus for self light-emitting display	US 6091398 A	345/204	345/598; 345/690; 358/1.9
22	20000704	19	Image display for dither halftoning	US 6084560 A	345/89	345/596; 345/599
23	20000530	54	Image processor using both dither and error diffusion to produce halftone images with less flicker and patterns	US 6069609 A	345/596	345/616; 345/690; 358/3.03; 382/252
24	20000516	18	Spatial light modulators	US 6064366 A	345/691	345/690; 345/84
25	20000418	10	Methods and apparatus for processing data values representative of an image with efficient dither matrices	US 6052113 A	345/596	345/598

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26	20000328	24	Display system with highly linear, flicker-free gray scales using high framecounts	US 6043801 A	345/89	345/589; 345/690
27	20000307	26	Method for providing grey scale images to the visible limit on liquid crystal displays	US 6034663 A	345/596	345/589; 345/602; 345/660; 345/690; 345/694; 345/696; 345/810
28	19991228	19	Flat-panel display controller with improved dithering and frame rate control	US 6008794 A	345/598	345/572; 345/694
29	19981006	9	Method and apparatus for reducing flicker in shaded displays	US 5818405 A	345/88	345/602; 345/694; 345/89
30	19980908	22	Display system with highly linear, flicker-free gray scales using high framecounts	US 5805126 A	345/89	345/694
31	19980721	45	Image information processor	US 5784040 A	345/89	345/690
32	19980127	17	Apparatus for performing a full-color emulation on the TFT display device	US 5712651 A	345/88	345/694; 345/89
33	19971111	17	Spatial light modulators	US 5686939 A	345/84	345/108; 345/55
34	19970121	43	Image information processor	US 5596349 A	345/690	345/89; 382/252

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35	19960227	10	Element generator for dither matrix and a dithering apparatus using the same	US 5495346 A	358/3.13	326/104; 326/52; 327/76; 340/146.2; 345/55; 345/596; 348/574; 358/3.23; 382/270; 708/625
36	19950214	13	Dithering circuit and method	US 5389948 A	345/691	
37	19940405	19	Window-relative dither circuit	US 5301269 A	345/599	
38	19931214	39	Image processing apparatus for estimating halftone images from bilevel and pseudo halftone images	US 5271095 A	345/428	345/589; 345/596; 345/643
39	19931019	25	Error propagated image halftoning with time-varying phase shift	US 5254982 A	345/690	345/89
40	19920616	31	System and method for blinking digitally-commanded pixels of a display screen to produce a palette of many colors	US 5122783 A	345/88	345/593; 345/611; 345/690
41	19900501	8	Matrix liquid crystal display with extended gray scale	US 4921334 A	345/89	358/443; 386/9
42	19880322	11	Method of displaying continuous tone picture using limited number of different colors or black-and-white levels, and display system therefor	US 4733230 A	345/589	345/690; 358/3.03
43	19761214	14	Bi-level display systems	US 3997719 A	348/798	315/169.4; 345/690
44	19760601	16	Bi-level display system	US 3961134 A	348/797	315/169.4; 345/596

	Issue Date	Page s	Title	Document ID	Current OR	Current XRef
45	19760210	13	Animated dithered display systems	US 3937878 A	348/798	315/169.4; 345/473; 345/596; 348/22
46	19751209	12	Animated display systems with dither threshold hysteresis band	US 3925609 A	348/798	327/76; 345/55

	Type	L #	Hits	Search Text	Dbs
1	BRS	L1	481	345/690.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	184	345/691-693.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L4	385	345/63.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L5	189	345/596-599.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L6	3749	345/87-89.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
6	BRS	L7	4699	1 or 2 or 4 or 5 or 6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
7	BRS	L8	608	345/55.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
8	BRS	L9	74150	(GREY ADJ SCALE) OR (GRAY ADJ SCALE) OR GRADATION OR HALF-TONE	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
9	BRS	L10	14884	DITHER\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
10	BRS	L11	132910	adder	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
11	BRS	L12	602	9 and 10 and 11	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Type	L #	Hits	Search Text	DBs
12	BRS	L13	5267	7 or 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
13	BRS	L14	46	12 and 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Issue Date	Pages	Title	Document ID	Current OR
1	20020702	47	Detector for detecting pseudo-contour noise and display apparatus using the detector	US 6414657 B1	345/63
2	20020604	33	Display apparatus capable of adjusting subfield number according to brightness	US 6400346 B2	345/63
3	20020514	34	Display apparatus capable of adjusting subfield number according to brightness	US 6388645 B2	345/63
4	20020507	33	Display apparatus capable of adjusting subfield number according to brightness	US 6384803 B2	345/63
5	20020305	32	Display apparatus capable of adjusting subfield number according to brightness	US 6353424 B1	345/63
6	20020226	33	Display apparatus capable of adjusting subfield number according to brightness	US 6351253 B1	345/63
7	20011218	34	Display apparatus capable of adjusting the number of subframes to brightness	US 6331843 B1	345/63
8	20000307	35	Plasma display panel and method of controlling brightness of the same	US 6034656 A	345/60

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1	20030506	17	Method for transferring image information, method for updating image information, transferring apparatus and updating apparatus	US 6559855 B1	345/596
2	20020326	20	Flat-panel display controller with improved dithering and frame rate control	US 6362834 B1	345/690
3	20001128	17	System and method for performing dithering with a graphics unit having an oversampling buffer	US 6154195 A	345/596
4	20001114	9	Temporally dissolved dithering	US 6147671 A	345/691
5	20000926	32	Force field halftoning	US 6124844 A	345/596
6	20000530	54	Image processor using both dither and error diffusion to produce halftone images with less flicker and patterns	US 6069609 A	345/596
7	20000516	25	Frame rate modulation for liquid crystal display (LCD)	US 6064359 A	345/89
8	20000418	10	Methods and apparatus for processing data values representative of an image with efficient dither matrices	US 6052113 A	345/596
9	19991228	19	Flat-panel display controller with improved dithering and frame rate control	US 6008794 A	345/598

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10	19990518	9	Generating color-correction look-up-table addresses by multi-level half-toning	US 5905490 A	345/597
11	19970408	19	System and method for real-time image display palette mapping	US 5619230 A	345/597
12	19970128	42	Method and apparatus for improved color recovery in a computer graphics system	US 5598184 A	345/597
13	19960227	10	Element generator for dither matrix and a dithering apparatus using the same	US 5495346 A	358/3.13
14	19941206	24	Method and apparatus for non-linear dithering of digital images	US 5371515 A	345/596
15	19931214	39	Image processing apparatus for estimating halftone images from bilevel and pseudo halftone images	US 5271095 A	345/428
16	19931123	26	Method and apparatus for vector aligned dithering	US 5264840 A	345/599
17	19921117	35	Method and apparatus for the dithering of antialiased vectors	US 5164717 A	345/596
18	19761214	14	Bi-level display systems	US 3997719 A	348/798